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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/929,734	08/13/2001	Geeta Desai Chennubhotla	ETEL-0003	9023

7590 03/09/2005  
WALKER & SAKO, LLP  
Suite 235  
300 South First Street  
San Jose, CA 95113

EXAMINER

RYMAN, DANIEL J

ART UNIT PAPER NUMBER

2665

DATE MAILED: 03/09/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No. 09/929,734	Applicant(s) CHENNUBHOTLA ET AL.	
	Examiner Daniel J. Ryman	Art Unit 2665	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 13 August 2001.
- 2a) ☐ This action is FINAL.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1-16 is/are allowed.
- 6) ☒ Claim(s) 17 and 18 is/are rejected.
- 7) ☒ Claim(s) 1, 19 and 20 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 13 August 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date <u>8/12/02</u> . | 6) <input type="checkbox"/> Other: _____  |

## **DETAILED ACTION**

### ***Specification***

1. The abstract of the disclosure is objected to because it exceeds 150 words in length.

Correction is required. See MPEP § 608.01(b).

2. The disclosure is objected to because of the following informalities: on page 9, line 13, "bit memory" should be "bit (JBVB) memory"; on page 14, line 23, "may have be formed" should be "may be formed"; on page 18, line 23, "Fig. 4B" should be "Fig. 6B"; on page 19, line 4, "Fig. 4B" should be "Fig. 6B"; on page 19, line 20, "Fig. 6" should be "Fig. 7"; on page 27, line 5, "1112" should be "1110"; and on page 27, line 6, "1112" should be "1110".

Appropriate correction is required.

### ***Claim Objections***

3. Claim 1 is objected to because of the following informalities: in line 5, "plurality valid" should be "plurality of valid". Appropriate correction is required.

### ***Claim Rejections - 35 USC § 103***

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 17 and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Richards et al. (USPN 6,480,499) in view of Ohlsson et al. (USPN 6,452,950).
6. Regarding claim 17, Richards discloses a method applicable in a jitter buffer system (col. 8, line 40-col. 9, line 2), comprising the steps of: providing a jitter buffer having a plurality of

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entries for storing data (col. 6, lines 5-11 and col. 9, lines 39-64); providing a valid memory (CAM) having a plurality of entries for storing status bits for each jitter buffer entry (Fig. 14 and col. 13, lines 41-65); receiving data for multiple channels (col. 6, lines 23-25); storing data segments for each channel in a corresponding group of jitter buffer entries (col. 6, lines 39-41); setting a status bit in the valid memory entry to indicate that an entry is valid for current use (col. 13, lines 49-50); and reading data from jitter buffer entry (col. 1, lines 6-9 and col. 8, line 40-col. 9, line 2) where it is implicit that the data is read from the jitter buffer.

Richards does not expressly disclose setting a status bit in the valid memory entry to a valid state when data is written to the corresponding jitter buffer entry; and setting the status bit of a valid buffer entry to an invalid state when data is read from the corresponding to the jitter buffer entry. Ohlsson teaches, in a system for jitter buffering, indicating a valid state (busy) for a memory entry when data is written to the corresponding jitter buffer entry; reading data from jitter buffer entry; and indicating an invalid state (free) when data is read from the corresponding to the jitter buffer entry (col. 2, lines 46-58 and col. 6, lines 24-58) in order to determine if a buffer entry is missing from the sequence of buffer entries (col. 6, lines 50-58). Thus, it would have been obvious to one of ordinary skill in the art at the time of the invention to set a status bit in the valid memory entry to a valid state when data is written to the corresponding jitter buffer entry and to set the status bit of a valid buffer entry to an invalid state when data is read from the corresponding to the jitter buffer entry in order to determine if a buffer entry is missing from the sequence of buffer entries.

7. Regarding claim 18, Richards in view of Ohlsson discloses receiving data includes receiving data packets having multiplexed data (Richards: col. 1, lines 6-9 and col. 6, lines 23-28 and Ohlsson: col. 1, lines 9-14 and col. 2, lines 21-27).

***Allowable Subject Matter***

8. Claims 1-16 are allowed. The prior art does not disclose or fairly suggest having an address generator that receives at least a first portion of a jitter buffer address to generate a corresponding valid bit store address.

9. Claims 19 and 20 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. The prior art does not disclose or fairly suggest that setting a status bit in a valid buffer entry includes swizzling the buffer address to generate the corresponding jitter buffer entry where swizzling is defined in the specification as "rearranging and/or performing one or more operations on a source address to generate a result address." Specification, pg. 19, lines 12-14.

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel J. Ryman whose telephone number is (571)272-3152. The examiner can normally be reached on Mon.-Fri. 7:00-4:30 with every other Friday off.

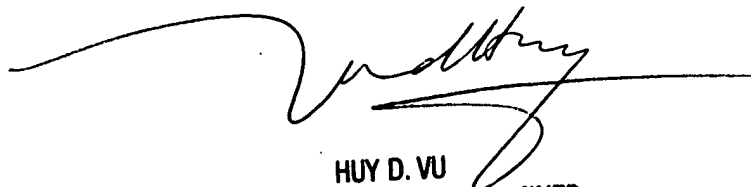
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Huy Vu can be reached on (571)272-3155. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Daniel J. Ryman  
Examiner  
Art Unit 2665

DSR

A handwritten signature in black ink, appearing to read 'Huy D. Vu', with a long horizontal line extending to the right.

HUY D. VU  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2600